



PATENT
Attorney Docket No.: SAM-0447
Customer No.: 29344

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Tae-seong Jang, *et al.*
Serial No.: 10/630,068
Filing Date: July 30, 2003
Patent Number: 6,950,365 B2
Issue Date: September 27, 2005
Examiner: Nguyen, Viet Q.
Group Art Unit: 2827
Title: SEMICONDUCTOR MEMORY DEVICE HAVING BITLINE
COUPLING SCHEME CAPABLE OF PREVENTING
DETERIORATION OF SENSING SPEED

Certificate
DEC 27 2005
of Correction

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

12/19/05

Date

Christina Harris

Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following:

1. Petition for Certificate of Correction under 37 C.F.R. § 1.322;
2. Certificate of Correction; and
3. Return Postcard

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

Date: 12/19/05
Mills & Onello, LLP
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Steven M. Mills
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PATENT
Attorney Docket No.: SAM-0447

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Christina Harris

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.322

Sir:

The patentee of the above-identified patent, through its attorney, hereby petitions for issuance of a Certificate of Correction in the above-identified patent. A certificate is required to correct the error in the claim as follows:

In Column 7, Claim 8, Line 24, please delete “,” after “semiconductor”.

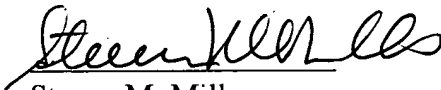
Applicant(s): Tae-seong Jang, *et al.*
Serial No.: 10/630,068

It is respectfully requested that a Certificate of Correction be issued to correct the error in the claims.

The correction is not believed to involve such change as would constitute new matter or require examination. Since the error is the fault of the United States Patent and Trademark Office, it is believed that no fee is due.

Respectfully submitted,

Date: 12/19/05
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Steven M. Mills
Registration Number 36,610
Attorney for Applicant

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.: 6,950,365 B2

DATED: September 27, 2005

INVENTOR(S): Tae-seong Jang, *et al.*

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 7, Claim 8, Line 24, please delete “,” after “semiconductor”.

MAILING ADDRESS OF SENDER:

Steven M. Mills
Mills & Onello LLP
Eleven Beacon Street, Suite 605
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PATENT NO. 6,950,365 B2